

Applicant: Dempsey Docket No: AD-332J Dempsey et al.

Serial No: 10/627,500

Title:

Integrated Digital Calibration Circuit and Digital to Analog Converter (DAC)
Jason D. Shanske, Reg. No. 43,915

Atty:

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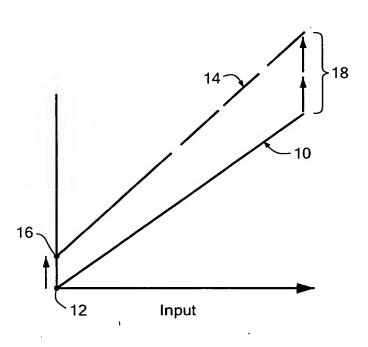
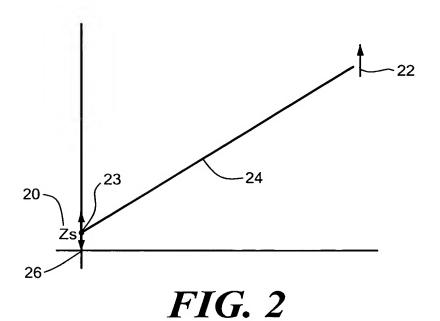


FIG. 1





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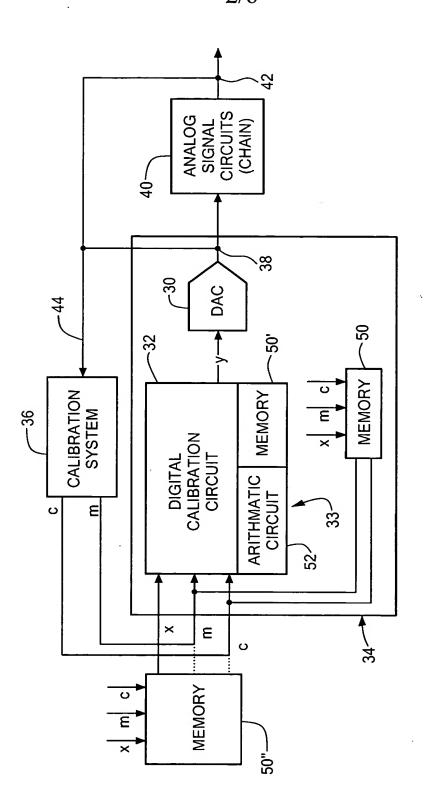


FIG 3



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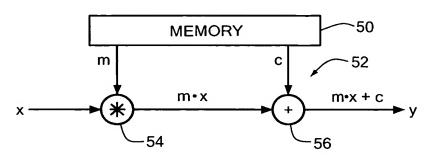


FIG. 4A

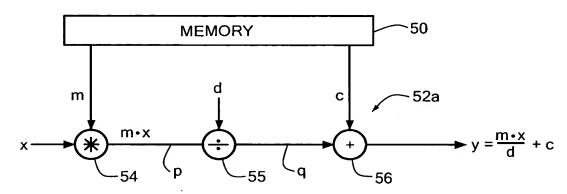


FIG. 4B

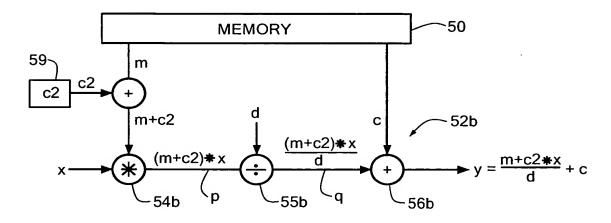


FIG. 4C



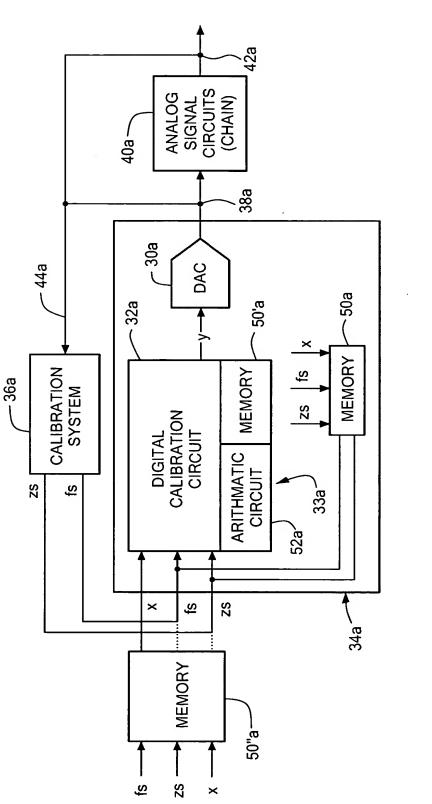
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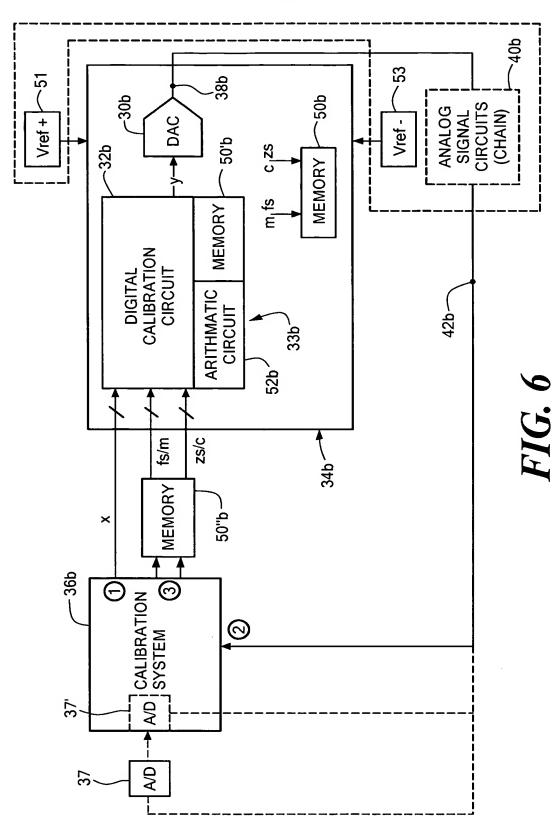
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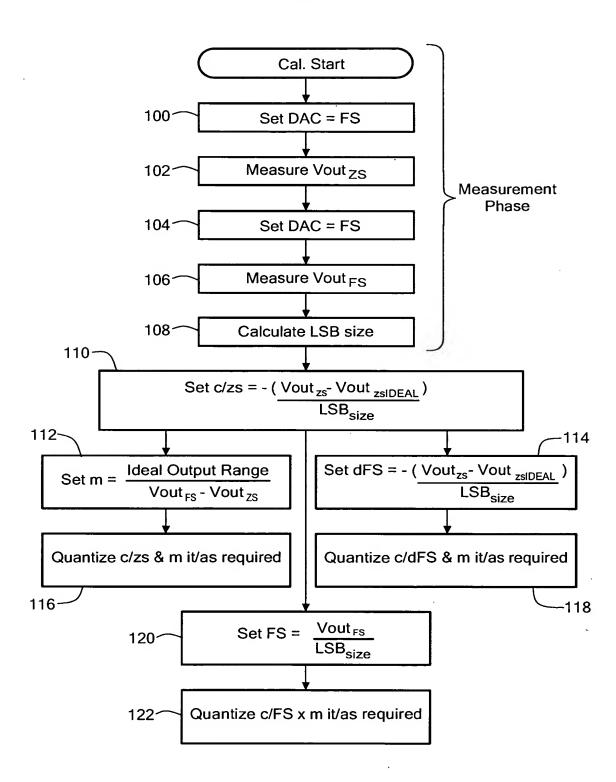


FIG 7